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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10006964	FILING DATE 12/05/2001	CLASS 257	SUBCLASS 620	GAU 2814	826	EXAMINER Dickey
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**APPLICANTS: Spooner Timothy; Harney Kieran; Courage David; Martih John;

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/251,287 12/05/2000
AND CLAIMS BENEFIT OF 60/251,288 12/05/2000

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	Analog 5721-3
Verified and Acknowledged Examiners's initials		
TITLE : Method and device for protecting micro electromechanical systems structures during dicing of a wafer		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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